

ABSTRACT OF THE DISCLOSURE

Regarding the time gradation method as the digital gradation expression method, it is an object of the invention to provide a display device which prevents a reduction of frame frequency and implements a low-power-consumption SRAM. The invention overcomes the
5 aforementioned disadvantages of conventional technique by synchronizing a writing and reading by utilizing read and write signals of which states read at a certain timing to select which of two memories is efficient to be written.